IEEE’s Hands on Practical Electronics (HOPE)
Week 9: CMOS, Digital Logic

Definitions:
CMOS: Complementary MOS. Uses both PMOS and NMOS in the same circuit.
Boolean Algebra: Developed by George Boole (1815-1864)
Logic: The study of the principles and criteria of valid inference
Gates: Examples: AND, OR, NOT, NAND, NOR
NAND: equivalent to an AND followed by a NOT
NOR: equivalent to an OR followed by a NOT

Conventions:
Yes or true are defined to be 1 or HIGH
No or false are defined to be 0 or LOW

Actual implementation of a NAND with transistors:

The top two transistors in parallel are PMOS, and the bottom two are NMOS.